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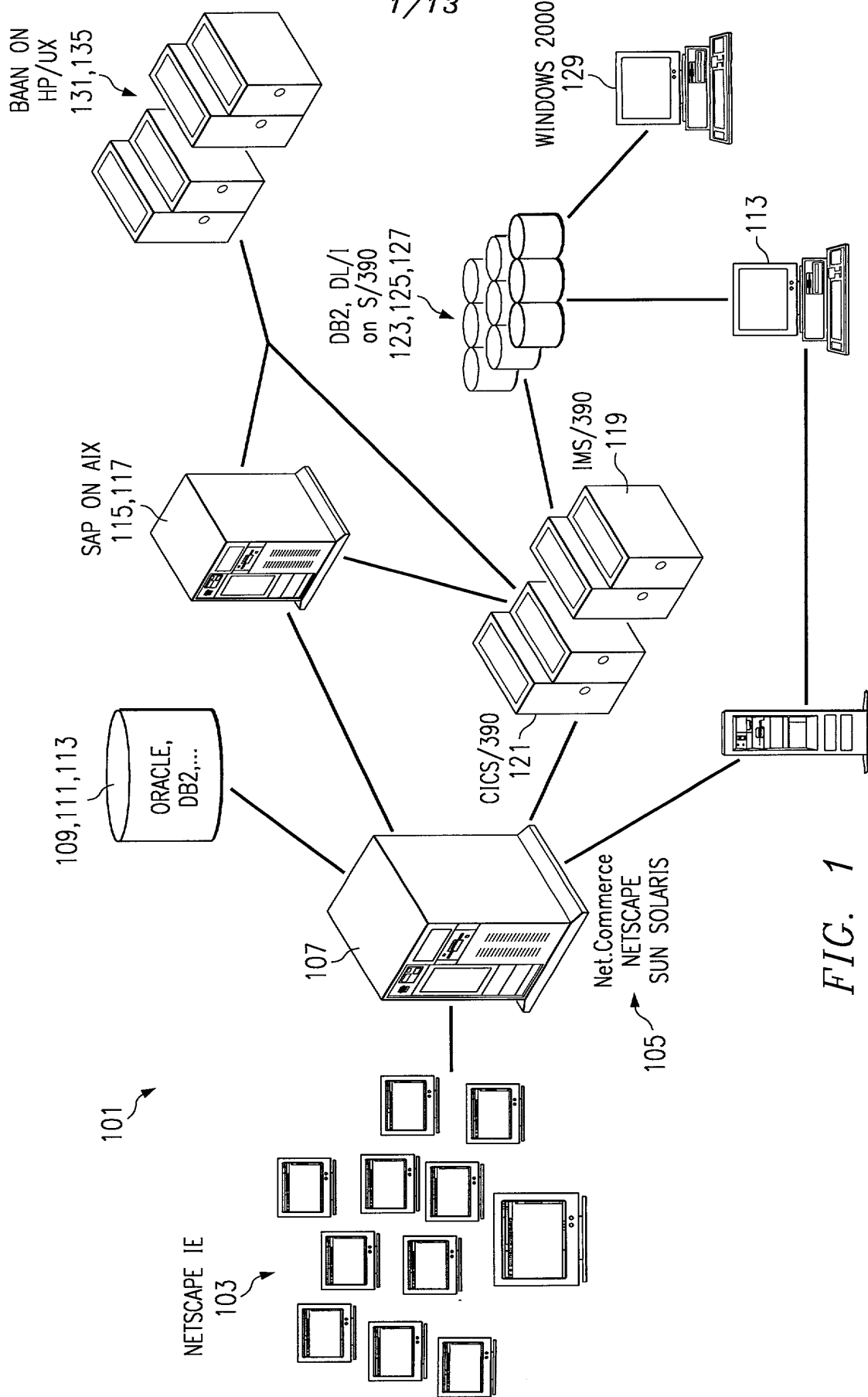


FIG. 1

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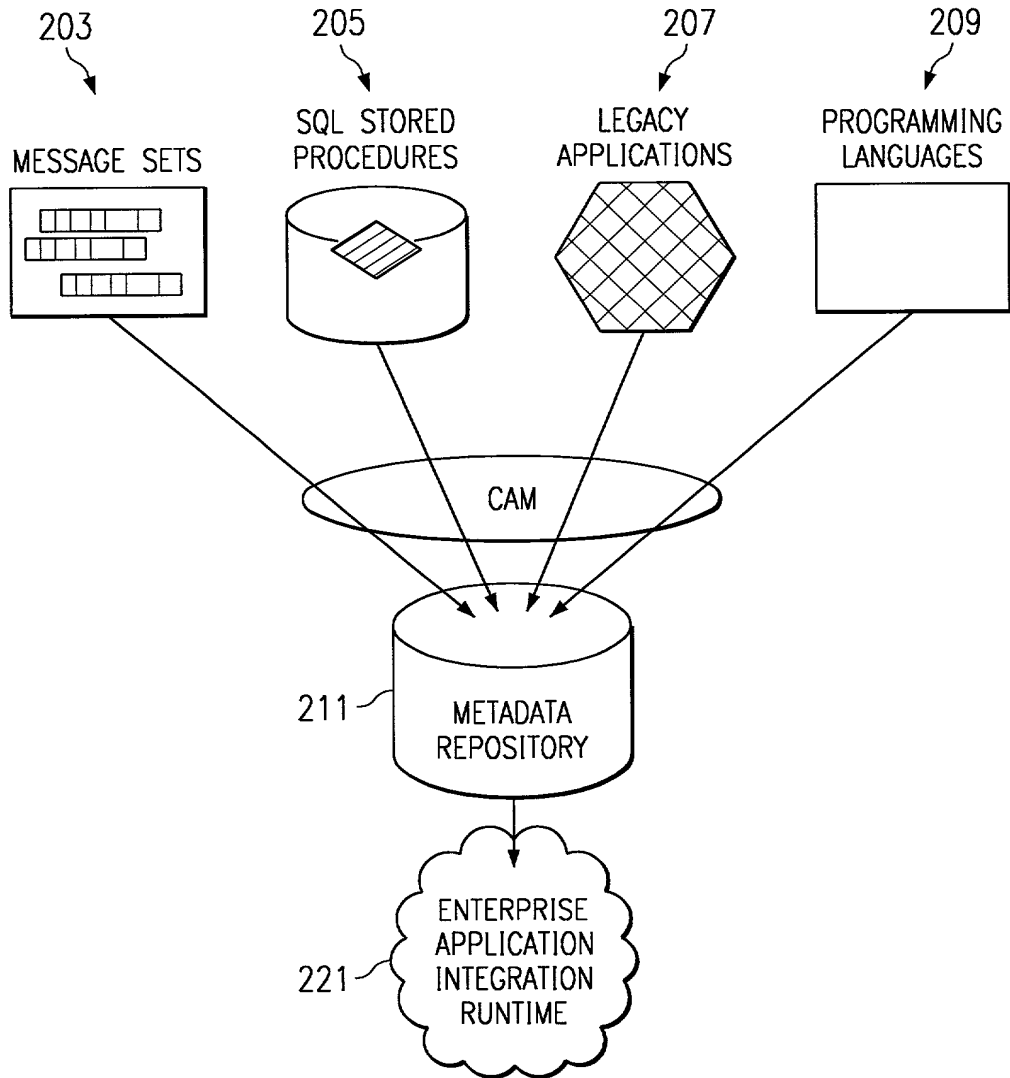


FIG. 2

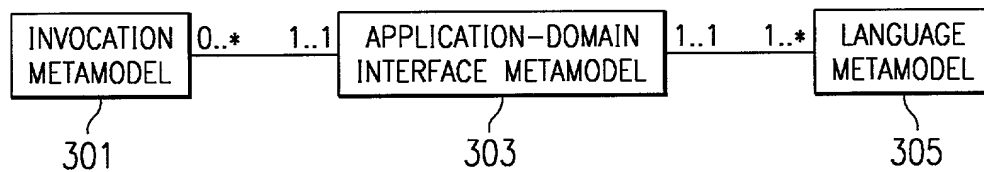
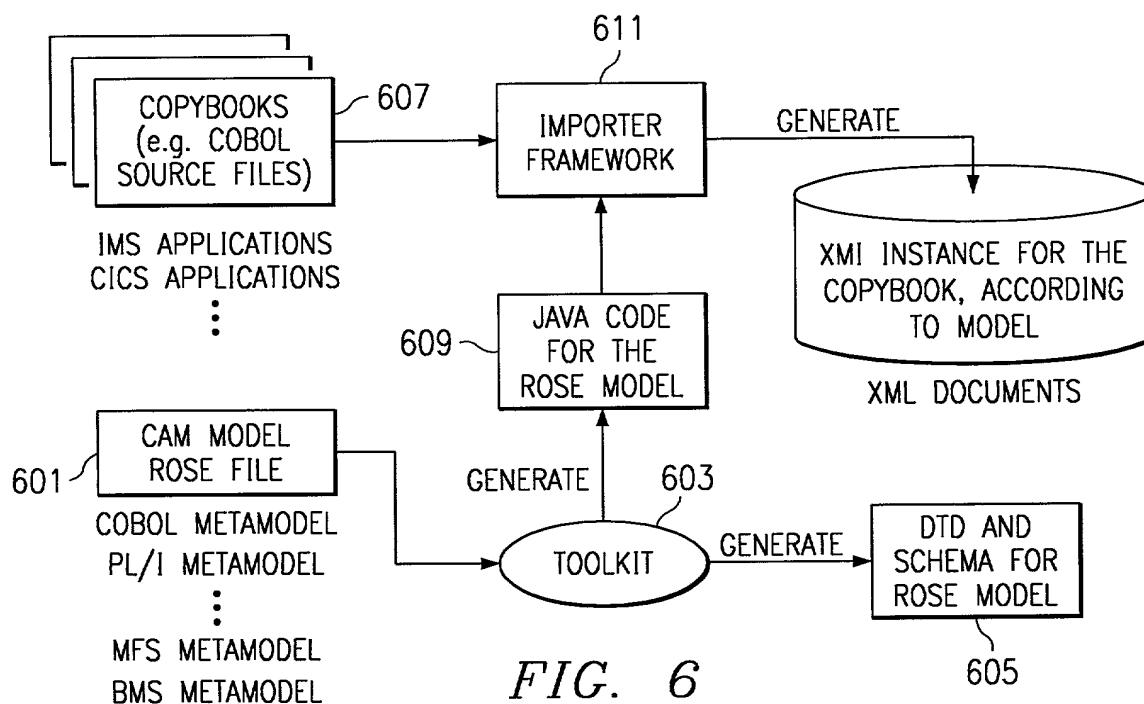
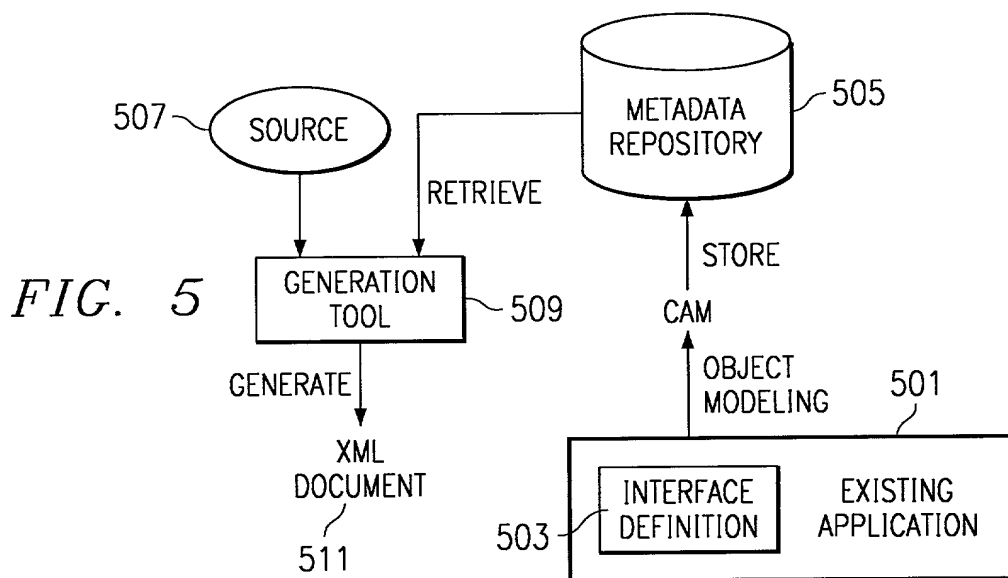
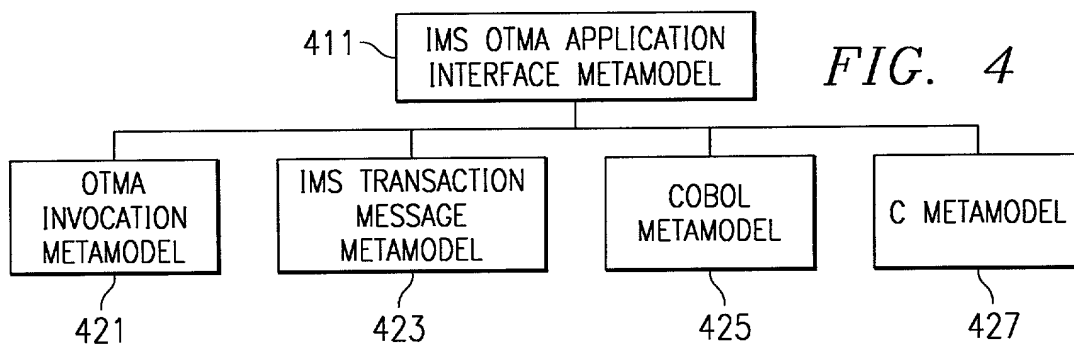


FIG. 3

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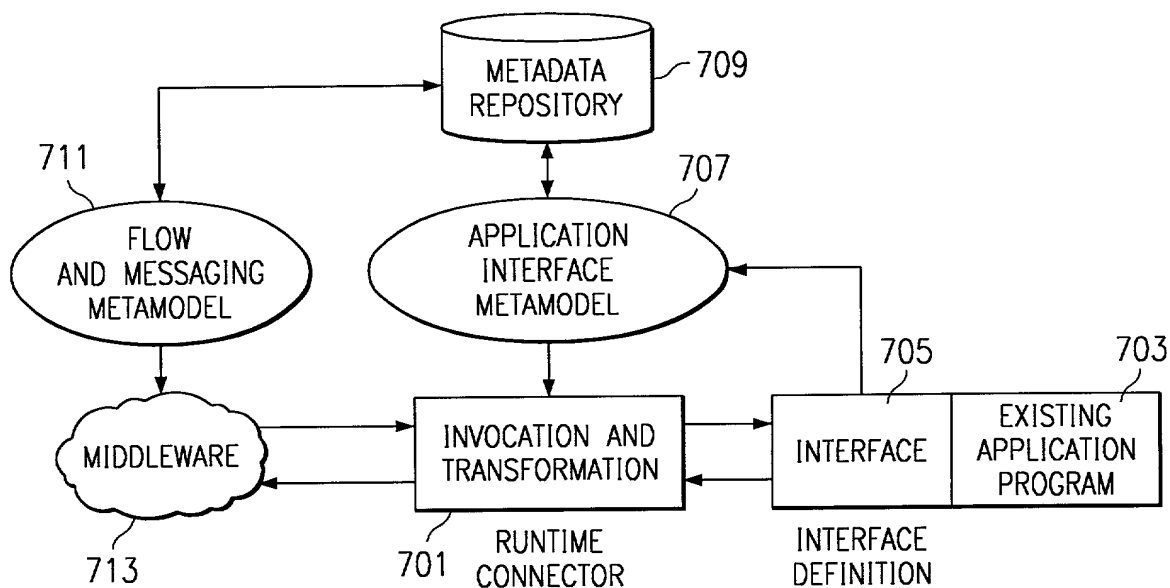


FIG. 7

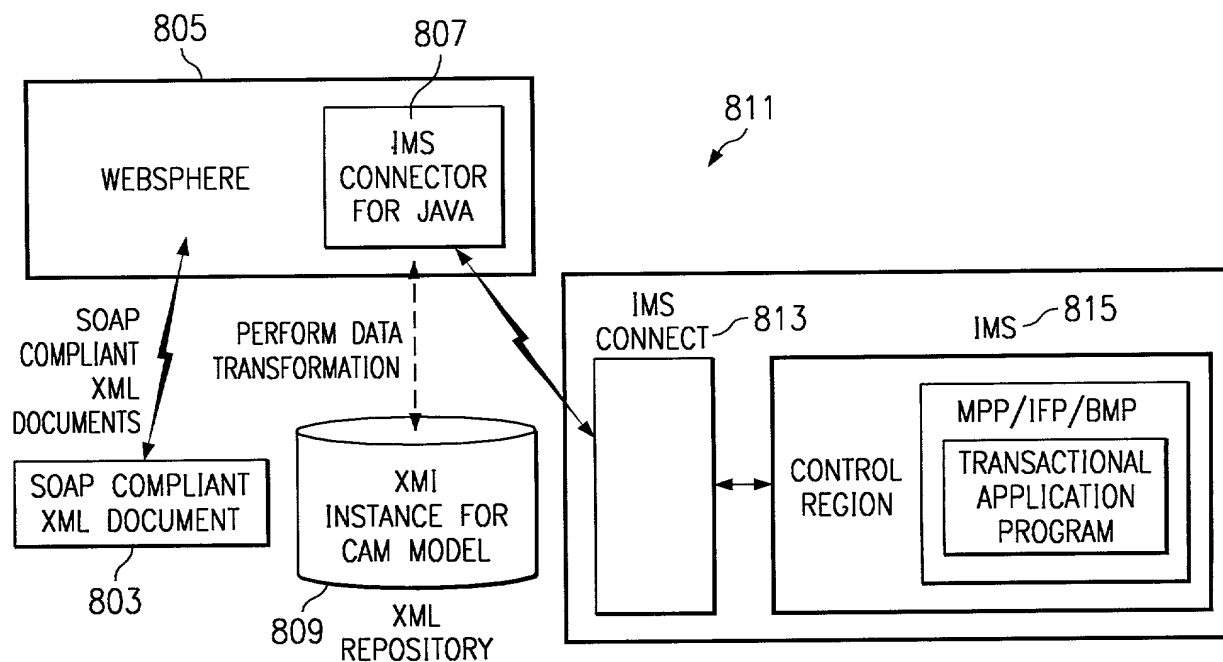
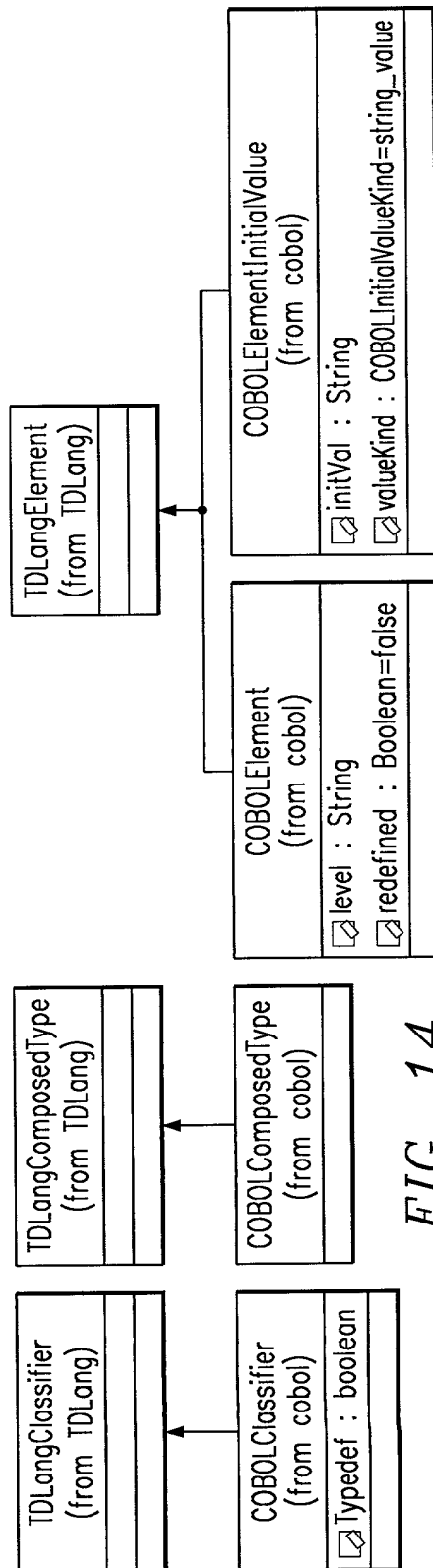
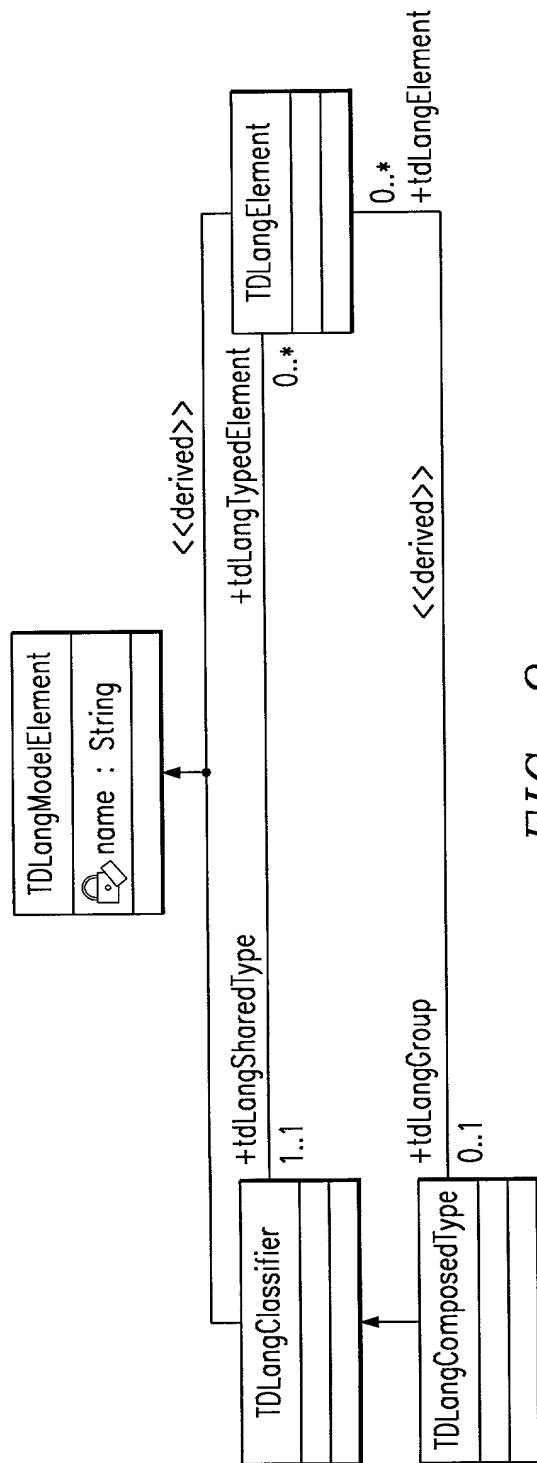
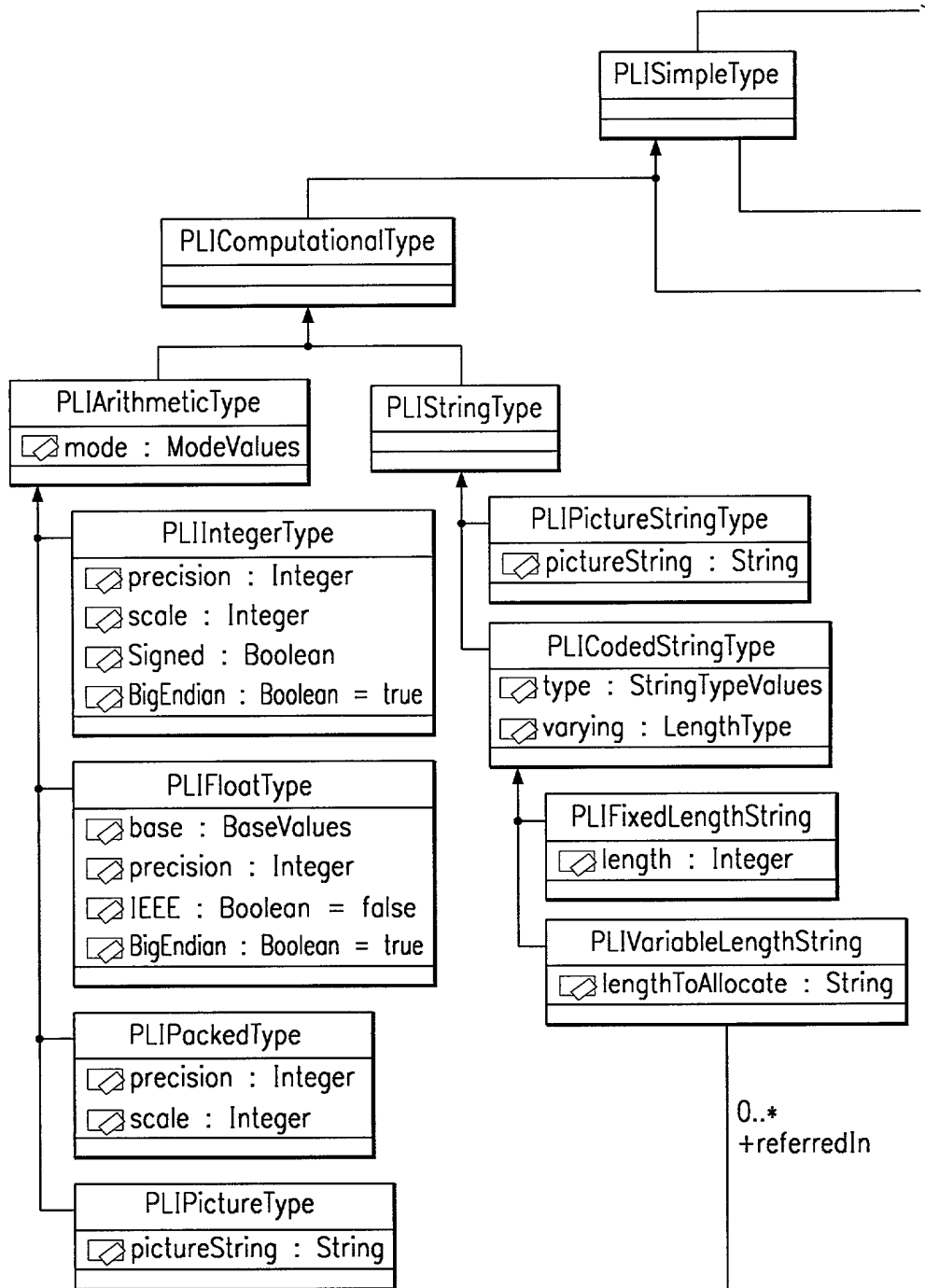


FIG. 8

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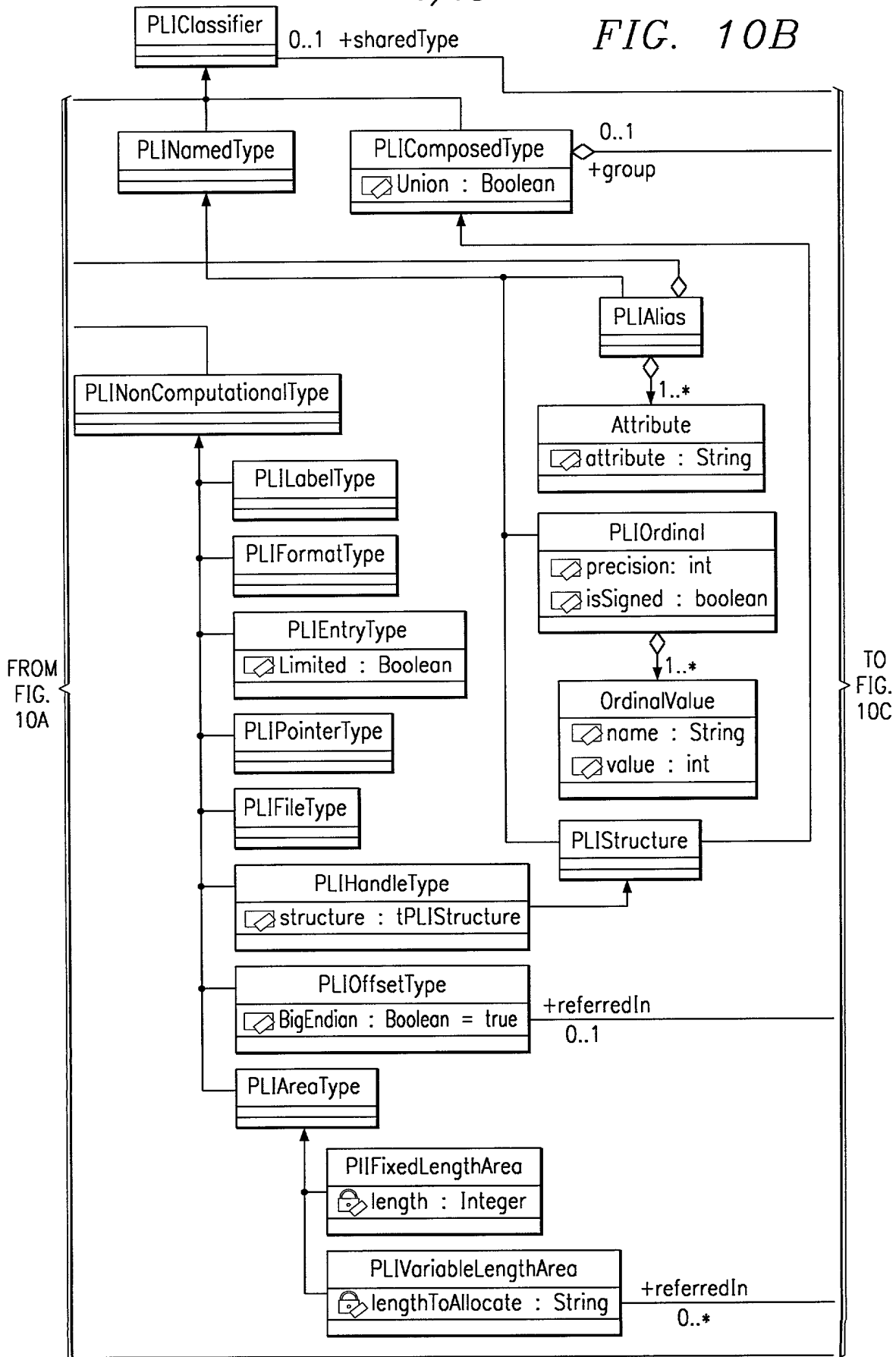


TO  
 FIG.  
 10B

FIG. 10A

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FIG. 10B



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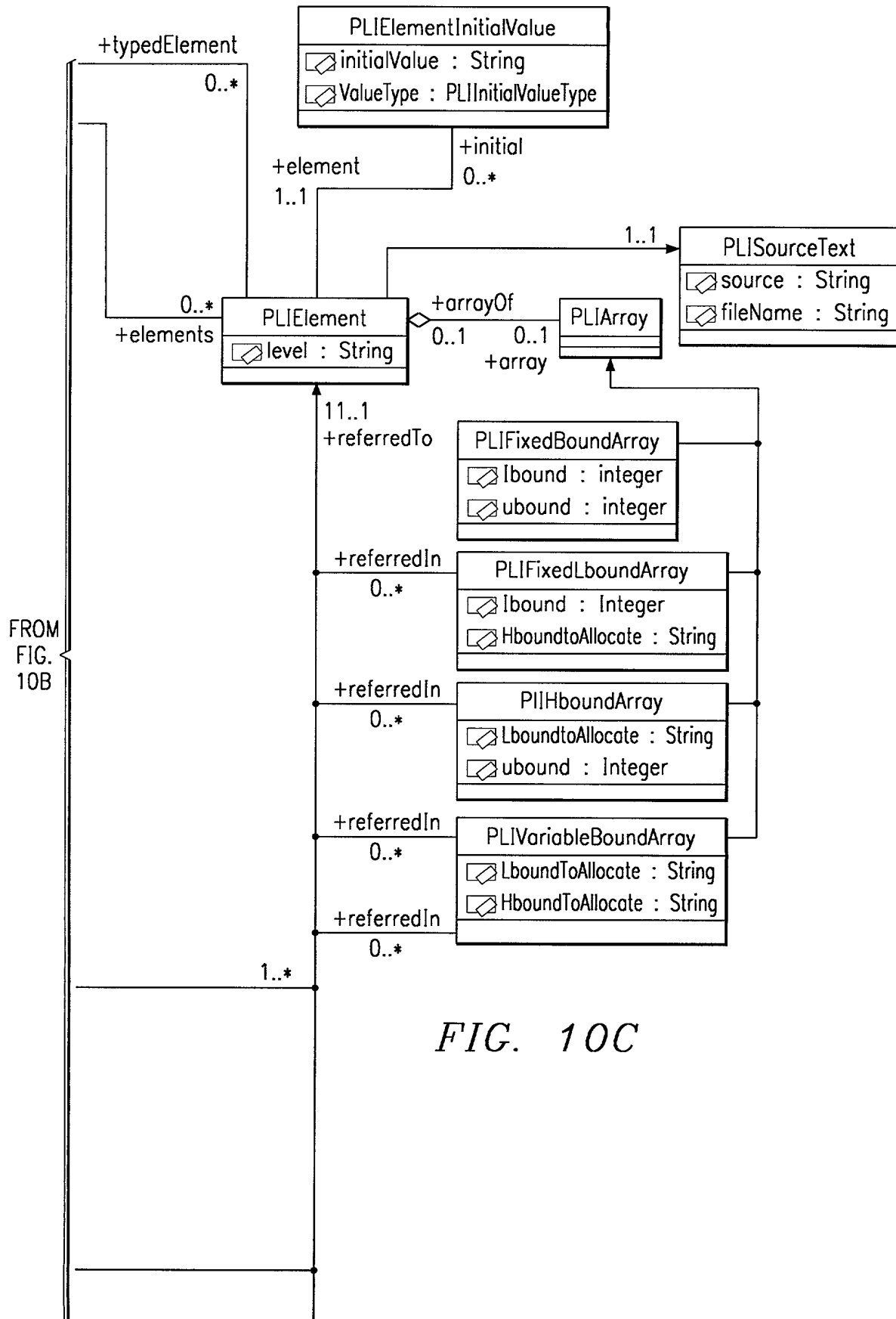


FIG. 10C



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FIG. 11

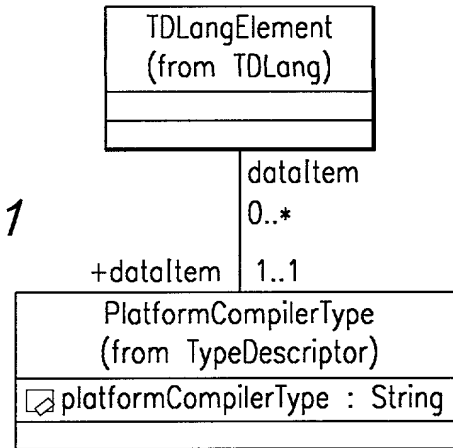


FIG. 15

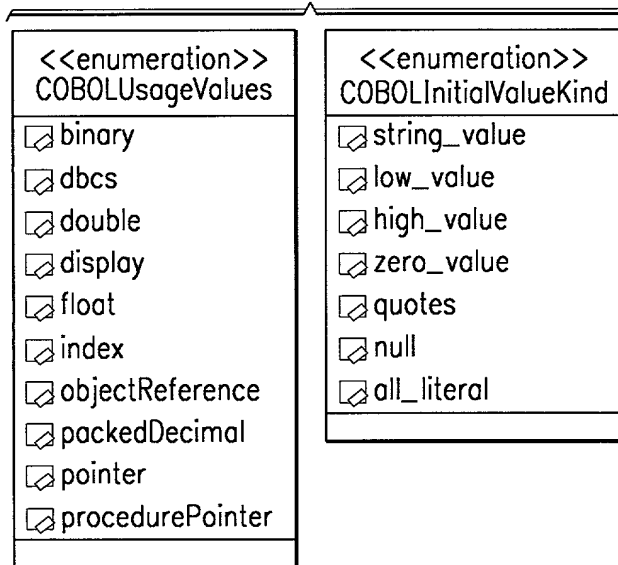
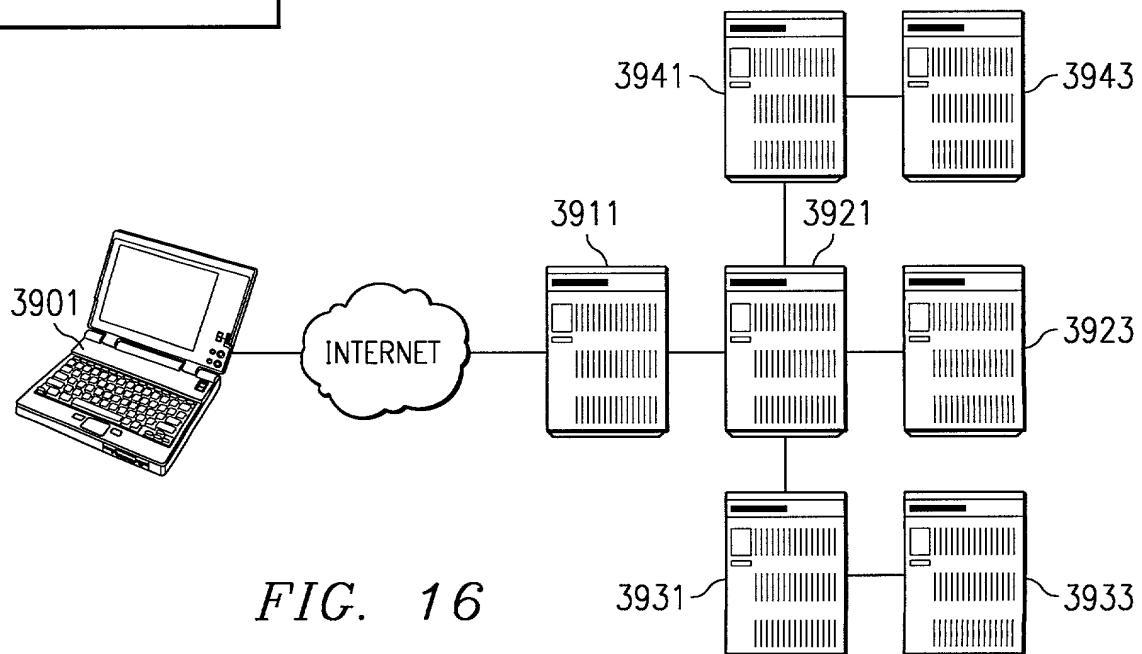


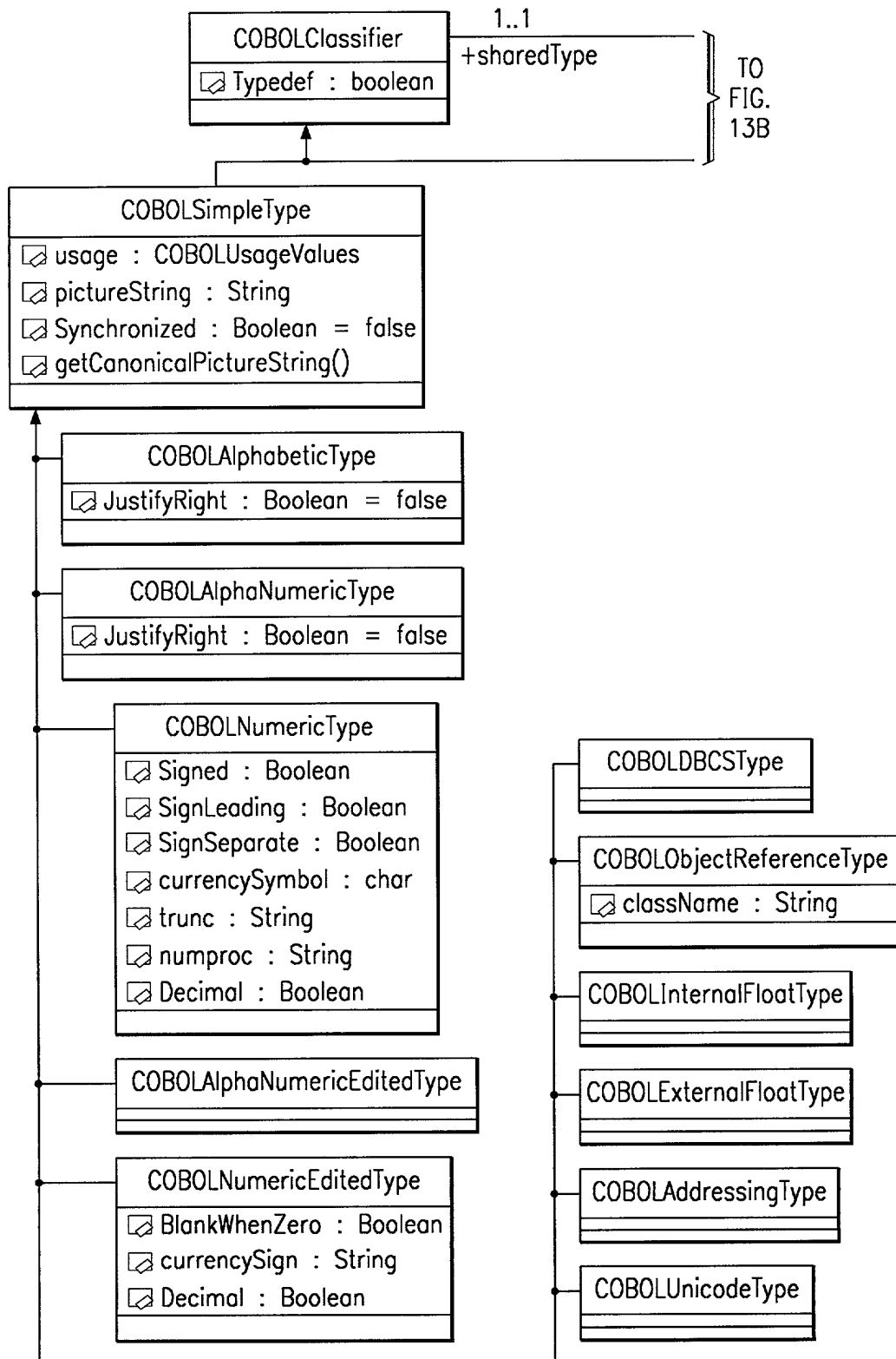
FIG. 16



<div>&lt;&lt;enumeration&gt;&gt; signCoding</div> <div><div><input checked="" type="checkbox"/> twosComplement</div><div><input checked="" type="checkbox"/> onesComplement</div><div><input checked="" type="checkbox"/> signMagnitude</div><div><input checked="" type="checkbox"/> zoneSigns</div><div><input checked="" type="checkbox"/> packedSigns</div><div><input checked="" type="checkbox"/> unsignedBinary</div><div><input checked="" type="checkbox"/> unsignedDecimal</div></div>	<div>&lt;&lt;enumeration&gt;&gt; lengthEncoding</div> <div><div><input checked="" type="checkbox"/> fixedLength</div><div><input checked="" type="checkbox"/> lengthPrefixed</div><div><input checked="" type="checkbox"/> nullTerminated</div></div>	<div>&lt;&lt;enumeration&gt;&gt; floatType</div> <div><div><input checked="" type="checkbox"/> Unspecified</div><div><input checked="" type="checkbox"/> IEEEExtendedIntel</div><div><input checked="" type="checkbox"/> IEEEExtendedAIX</div><div><input checked="" type="checkbox"/> IEEEExtendedOS390</div><div><input checked="" type="checkbox"/> IEEEExtendedAS400</div><div><input checked="" type="checkbox"/> IEEEnonextended</div><div><input checked="" type="checkbox"/> IBM390Hex</div><div><input checked="" type="checkbox"/> IBM400Hex</div></div>	<div>&lt;&lt;enumeration&gt;&gt; accessor</div> <div><div><input checked="" type="checkbox"/> ReadOnly</div><div><input checked="" type="checkbox"/> WriteOnly</div><div><input checked="" type="checkbox"/> ReadWrite</div><div><input checked="" type="checkbox"/> NoAccess</div></div>	<div>&lt;&lt;enumeration&gt;&gt; packedDecimalSign</div> <div><div><input checked="" type="checkbox"/> MVS</div><div><input checked="" type="checkbox"/> MVSCustom</div><div><input checked="" type="checkbox"/> NT_OS2_AIX</div></div>	<div>&lt;&lt;enumeration&gt;&gt; bitModePad</div> <div><div><input checked="" type="checkbox"/> mode16</div><div><input checked="" type="checkbox"/> mode24</div><div><input checked="" type="checkbox"/> mode31</div><div><input checked="" type="checkbox"/> mode32</div><div><input checked="" type="checkbox"/> mode64</div><div><input checked="" type="checkbox"/> mode128</div></div>
<div>&lt;&lt;enumeration&gt;&gt; addrUnit</div> <div><div><input checked="" type="checkbox"/> bit</div><div><input checked="" type="checkbox"/> byte</div><div><input checked="" type="checkbox"/> word</div><div><input checked="" type="checkbox"/> doubleWord</div></div>					

FIG. 12

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TO  
FIG.  
13B

FIG. 13A

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 T06070 ETB64860

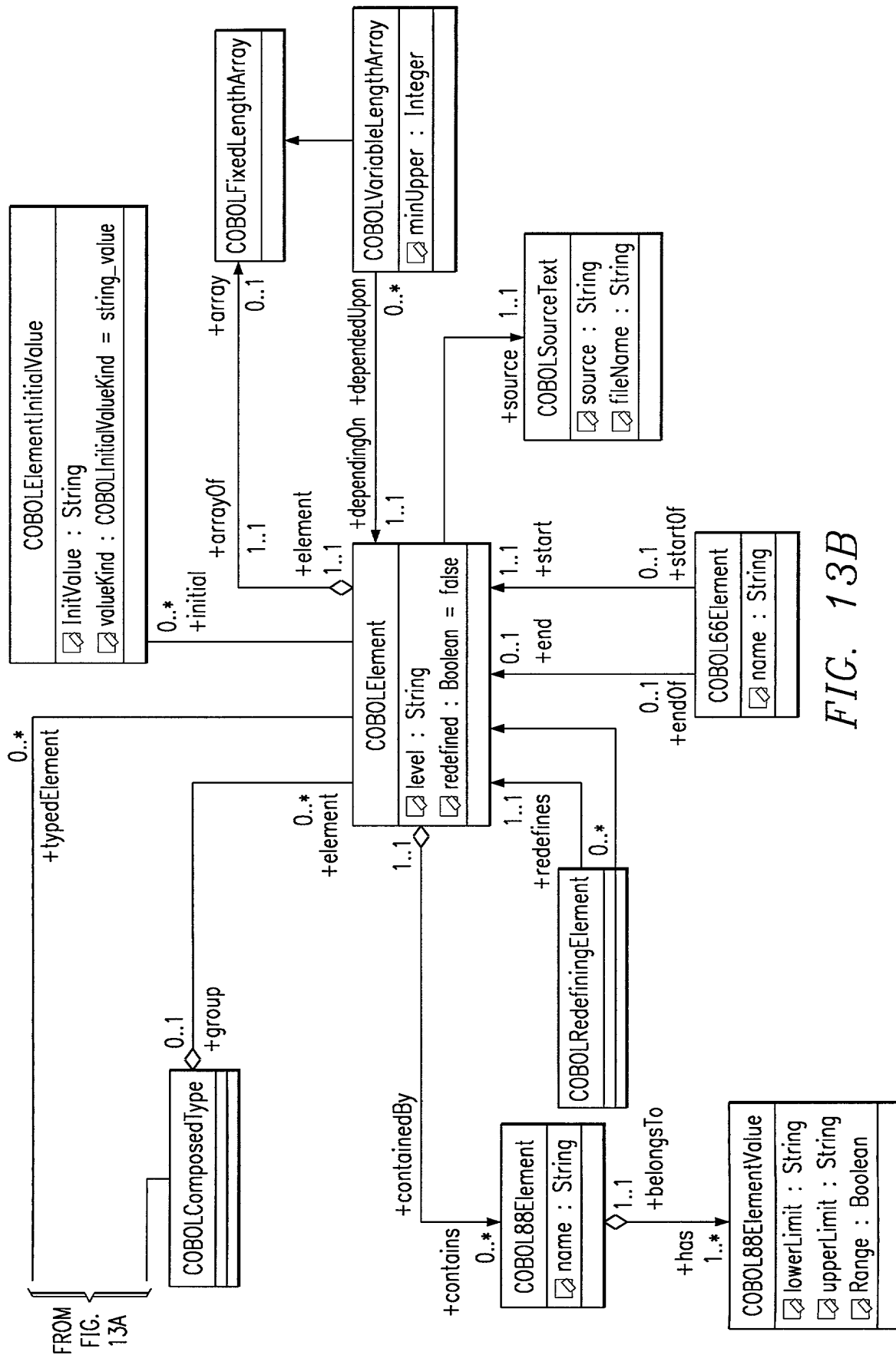


FIG. 13B

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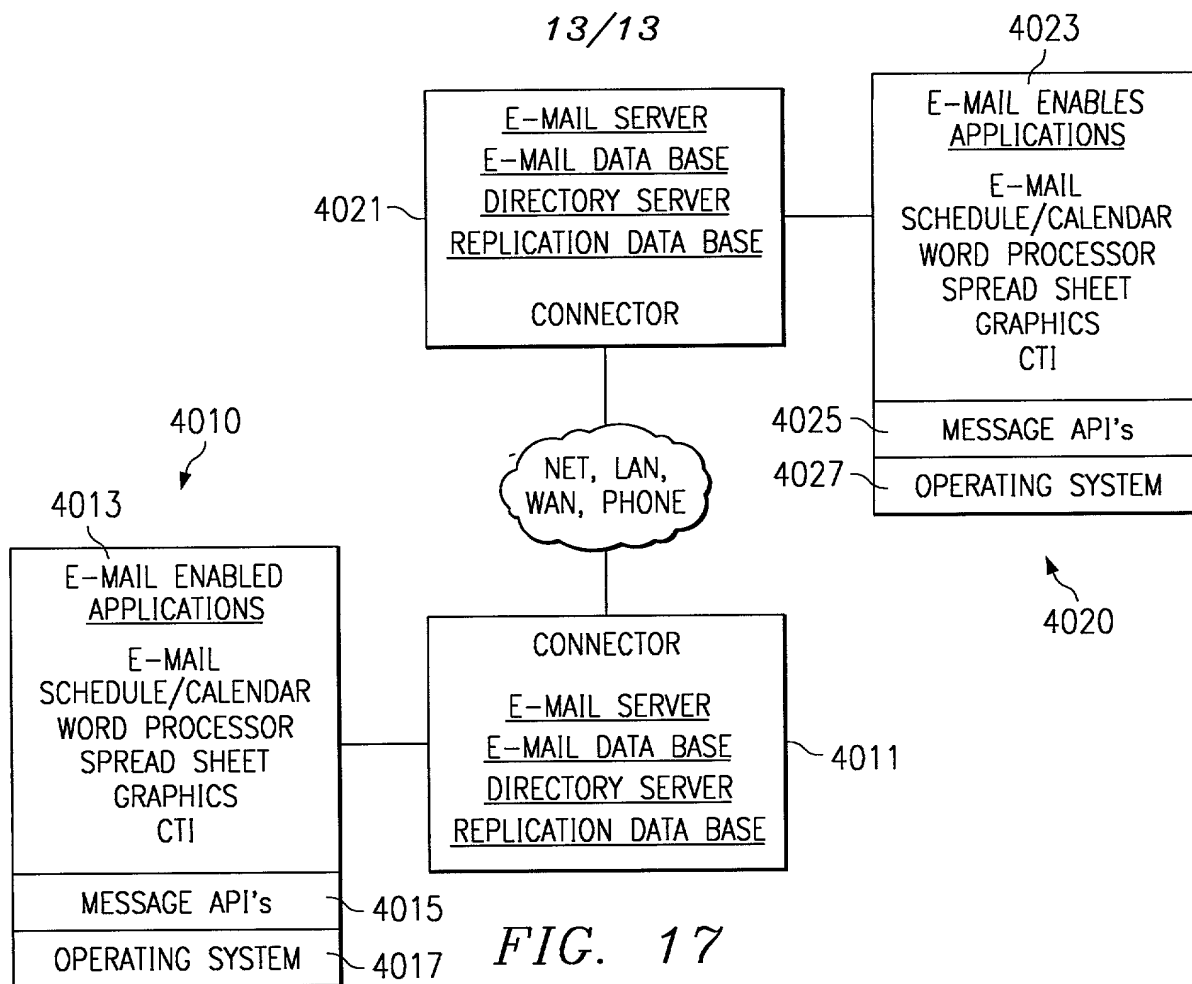


FIG. 17

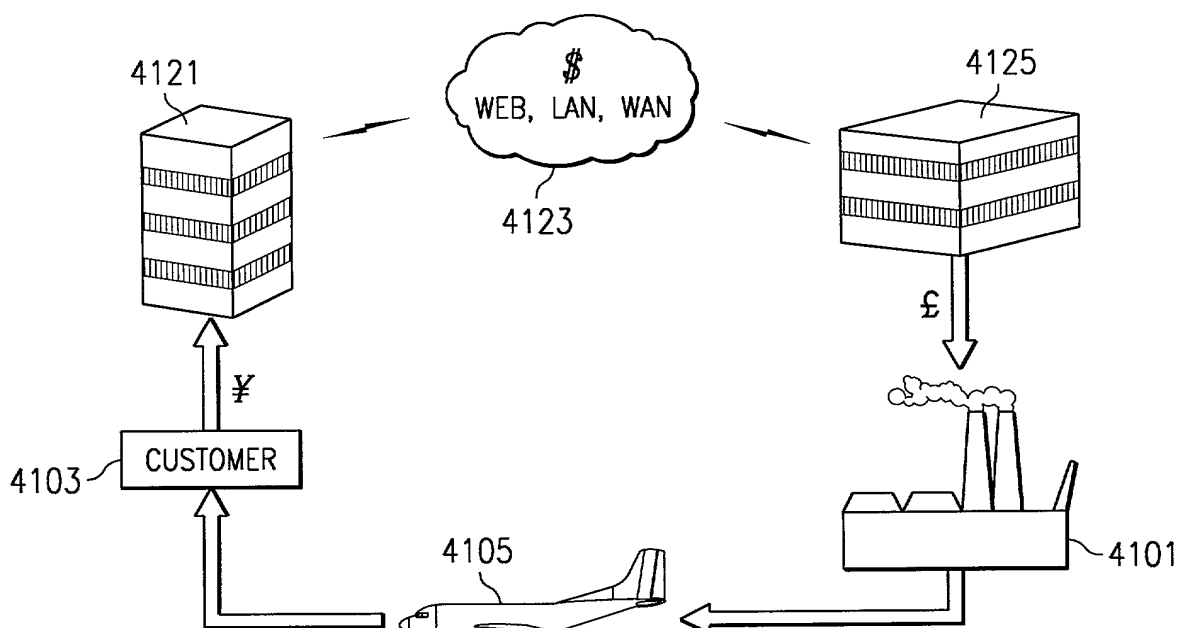


FIG. 18